

Figure 2: Interface to Host PCB

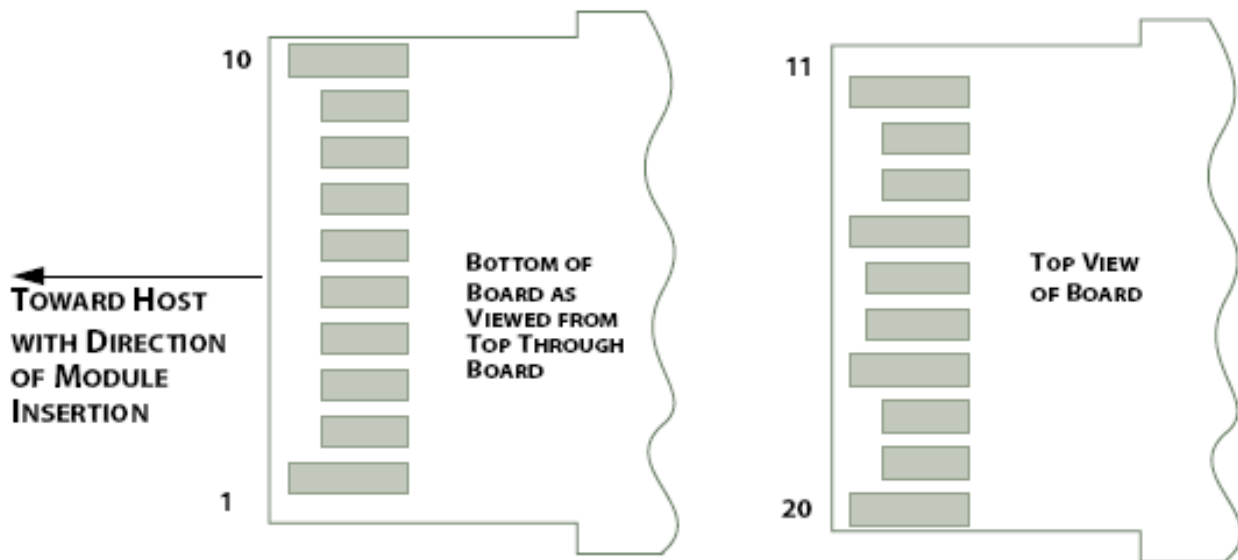


Figure 3: Module Contact Assignment

Contacts	logic	Symbol	Power Sequence Order	Name/Description
1		VeeT	1st	Module Transmitter Ground
2	LVTTL-O	TX_Fault	3rd	Module Transmitter Fault
3	LVTTL-I	TX_Disable	3rd	Transmitter Disable; Turns off transmitter laser output
4	LVTTL-I/O	SDA	3rd	2-wire Serial Interface Data Line (Same as MOD-DEF2 in the INF-8074i)
5	LVTTL-I/O	SCL	3rd	2-wire Serial Interface Clock (Same as MOD-DEF1 in the INF-8074i)
6		Mod_ABS	3rd	Module Absent, connected to VeeT or VeeR in the module
7	LVTTL-I	RS0	3rd	Rate Select 0, optionally controls SFP+ module receiver. When high input signaling rate > 4.25 GBd and when low input signaling rate ≤ 4.25 GBd.
8	LVTTL-O	Rx_LOS	3rd	Receiver Loss of Signal Indication (In FC designated as Rx_LOS and in Ethernet designated as Signal Detect)
9	LVTTL-I	RS1	3rd	Rate Select 1, optionally controls SFP+ transmitter. When high input signaling rate > 4.25 GBd and when low input signaling rate ≤ 4.25 GBd.
10		VeeR	1st	Module Receiver Ground
11		VeeR	1st	Module Receiver Ground
12	CML-O	RD-	3rd	Receiver Inverted Data Output
13	CML-O	RD+	3rd	Receiver Non-Inverted Data Output
14		VeeR	1st	Module Receiver Ground
15		VccR	2nd	Module Receiver 3.3 V Supply
16		VccT	2nd	Module Transmitter 3.3 V Supply
17		VeeT	1st	Module Transmitter Ground
18	CML-I	TD+	3rd	Transmitter Non-Inverted Data Input
19	CML-I	TD-	3rd	Transmitter Inverted Data Input
20		VeeT	1st	Module Transmitter Ground

Table 2: SFP+ Module PIN Definition

Absolute Maximum Ratings

These values represent the damage threshold of the module. Stress in excess of any of the individual Absolute Maximum Ratings can cause immediate catastrophic damage to the module even if all other parameters are within Recommended Operating Conditions.

Parameters	Symbol	Min.	Max.	Unit
Power Supply Voltage	VCC	0	+3.6	V
Storage Temperature	Tc	-40	+85	°C
Operating Case Temperature	Tc	0	+70	°C
Relative Humidity	RH	5	95	%
RX Input Average Power	Pmax	-	0	dBm

Table 3: Absolute Maximum Rating

Recommended operating environment

Recommended Operating Environment specifies parameters for which the electrical and optical characteristics hold unless otherwise noted.



Parameter	Symbol	Min.	Typical	Max	Unit
Power Supply Voltage	V_{CC}	3.135	3.300	3.465	V
Operating Case Temperature	T_C	0	25	70	°C

Table 4: Recommended Operating Environment

Optical Characteristics

The following optical characteristics are defined over the Recommended Operating Environment unless otherwise specified.

Parameter	Symbol	Min.	Typical	Max	Unit	Notes
Transmitter						
Center Wavelength	λ_t	840	850	860	nm	
RMS spectral width	P_m	-	-	4	nm	
Average Optical Power	P_{avg}	-5	-	-1	dBm	2
Extinction Ratio	ER	3	-	-	dB	3
Transmitter Dispersion Penalty	TDP	-	-	3.9	dB	
Relative Intensity Noise	R_{in}	-	-	-128	dB/Hz	12dB reflection
Optical Return Loss Tolerance		-	-	12	dB	
Receiver						
Center Wavelength	λ_r	840	850	860	nm	
Receiver Sensitivity	P_{sens}	-	-	-11	dBm	4
Stressed Sensitivity in OMA		-	-	-8	dBm	4
Los function	Los	-30	-	-12	dBm	
Overload	P_{in}	-	-	0	dBm	4
Receiver Reflectance		-	-	-12	dB	
LOS De-Assert	LOSD			-13	dBm	
LOS Assert	LOSA	-23			dBm	

Notes:

- 1.Trade-offs are available between spectral width, center wavelength and minimum OMA, as shown in table 6.
- 2.The optical power is launched into MMF
- 3.Measured with a PRBS $2^{31}-1$ test pattern @8.5Gbps
- 4.Measured with a PRBS $2^{31}-1$ test pattern @8.5Gbps, $BER \leq 10^{-12}$.

Table 5: Optical Characteristics

Center Wavelength (nm)	RMS Spectral width (nm)								
	Up to 0.05	0.05 to 0.1	0.1 to 0.15	0.15 to 0.2	0.2 to 0.25	0.25 to 0.3	0.3 to 0.35	0.35 to 0.4	0.4 to 0.45
840 to 842	-4.2	-4.2	-4.1	-4.1	-3.9	-3.8	-3.5	-3.2	-2.8
842 to 844	-4.2	-4.2	-4.2	-4.1	-3.9	-3.8	-3.6	-3.3	-2.9
844 to 846	-4.2	-4.2	-4.2	-4.1	-4.0	-3.8	-3.6	-3.3	-2.9
846 to 848	-4.3	-4.2	-4.2	-4.1	-4.0	-3.8	-3.6	-3.3	-2.9
848 to 850	-4.3	-4.2	-4.2	-4.1	-4.0	-3.8	-3.6	-3.3	-3.0
850 to 852	-4.3	-4.2	-4.2	-4.1	-4.0	-3.8	-3.6	-3.4	-3.0
852 to 854	-4.3	-4.2	-4.2	-4.1	-4.0	-3.9	-3.7	-3.4	-3.1
854 to 856	-4.3	-4.3	-4.2	-4.1	-4.0	-3.9	-3.7	-3.4	-3.1
856 to 858	-4.3	-4.3	-4.2	-4.1	-4.0	-3.9	-3.7	-3.5	-3.1
858 to 860	-4.3	-4.3	-4.2	-4.2	-4.1	-3.9	-3.7	-3.5	-3.2

Table 6: Minimum 10GBASE-SR OMA as a Function of Wavelength and Spectral Width

Digital Diagnostic Functions

The following digital diagnostic characteristics are defined over the Recommended Operating Environment unless otherwise specified. It is compliant to SFF8472 Rev9.2 with internal calibration mode. For external calibration mode please contact our sales staff.

Parameter	Symbol	Min.	Max	Unit	Notes
Temperature monitor absolute error	DMI_Temp	-3	+3	degC	Over operating temp
Laser power monitor absolute error	DMI_TX	-3	+3	dB	
RX power monitor absolute error	DMI_RX	-3	+3	dB	-3dBm to -12dBm range
Supply voltage monitor absolute error	DMI_VCC	-0.08	+0.08	V	Full operating range
Bias current monitor	DMI_Ibias	-10%	10%	mA	

Electrical characteristics

The following electrical characteristics are defined over the Recommended Operating Environment unless otherwise specified.

Parameter	Symbol	Min.	Typical	Max	Unit	Notes
Data Rate		-	8.5	-	Gbps	
Power Consumption		-	600	800	mW	
Transmitter						
Single Ended Output Voltage Tolerance		-0.3	-	4.0	V	
C common mode voltage tolerance		15	-	-	mV	



Tx Input Diff Voltage	VI	180		1200	mV	
Tx Fault	VoL	-0.3		0.4	V	At 0.7mA
Data Dependent Input Jitter	DDJ			0.10	UI	
Data Input Total Jitter	TJ			0.28	UI	
Receiver						
Single Ended Output Voltage Tolerance		-0.3	-	4.0	V	
Rx Output Diff Voltage	Vo	300		850	mV	
Rx Output Rise and Fall	Tr/Tf	30			ps	20% to 80%
Total Jitter	TJ			0.70	UI	
Deterministic Jitter	DJ			0.42	UI	

Table 7: Electrical Characteristics

Control and status I/O timing Characteristics

Timing characteristics of control and status I/O are included in Table 8, which is also defined in SFF-8431.

Parameter	Symbol	Min.	Max.	Unit	Conditions
TX_Disable assert time	t _{off}		100	μs	rising edge of TX_Disable to fall of output signal below 10% of nominal
TX_Disable negate time	t _{on}		2	ms	Falling edge of Tx_Disable to rise of output signal above 90% of nominal. This only applies in normal operation, not during start up or fault recovery.
Time to initialize 2-wire interface	t _{2w_start_up}		300	ms	From power on or hot plug after the supply meeting Table 8 .
Time to initialize	t _{start_up}		300	ms	From power supplies meeting Table 8 or hot plug or Tx disable negated during power up, or Tx_Fault recovery, until non-cooled power level I part (or non-cooled power level II part already enabled at power level II for Tx_Fault recovery) is fully operational.
Time to initialize cooled module	t _{start_up_cooled}		90	s	From power supplies meeting Table 8 or hot plug, or Tx disable negated during power up or Tx_Fault recovery, until cooled power level I part (or cooled power level II part during fault recovery) is fully operational.
Time to Power Up to Level II	t _{power_level2}		300	ms	From falling edge of stop bit enabling power level II until non-cooled module is fully operational
Time to Power Down from Level II	t _{power_down}		300	ms	From falling edge of stop bit disabling power level II until module is within power level I requirements
TX_Fault assert	TX_Fault_on		1	ms	From occurrence of fault to assertion of TX_Fault
TX_Fault assert for cooled module	TX_Fault_on_cooled		50	ms	From occurrence of fault to assertion of TX_Fault
TX_Fault Reset	t _{reset}	10		μs	Time TX_Disable must be held high to reset TX_Fault
RS0, RS1 rate select timing for FC	t _{RS0_FC, RS1_FC}		500	μs	From assertion till stable output
RS0, RS1 rate select timing non FC	t _{RS0, t_RS1}		10	ms	From assertion till stable output
Rx_LOS assert delay	t _{los_on}		100	μs	From occurrence of loss of signal to assertion of Rx_LOS
Rx_LOS negate delay	t _{los_off}		100	μs	From occurrence of presence of signal to negation of Rx_LOS

Table 8: Timing Characteristics

Mechanical

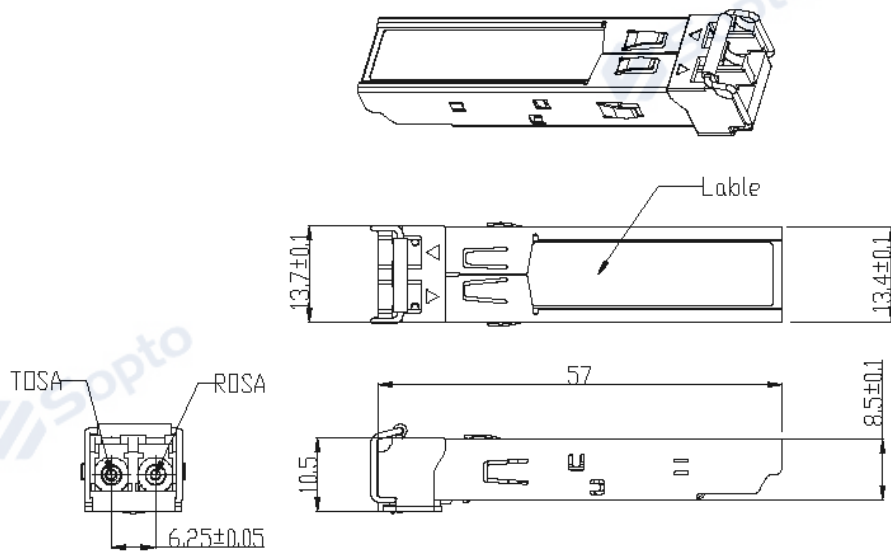


Table 9: Key Mechanical Dimensions

ESD

This transceiver is specified as ESD threshold 2kV for all electrical input pins, tested per MIL-STD-883, Method 3015.4 /JESD22-A114-A (HBM). However, normal ESD precautions are still required during the handling of this module. This transceiver is shipped in ESD protective packaging. It should be removed from the packaging and handled only in an ESD protected environment.

Laser Safety

This is a Class 1 Laser Product according to IEC 60825-1:1993:+A1:1997+A2:2001. This product complies with 21 CFR 1040.10 and 1040.11 except for deviations pursuant to Laser Notice No. 50, dated (July 26, 2001)

Ordering information

Part Number	Product Description
SPT-P858G-S3D	Transceiver SFP+ 850nm 8.5G LC 300m with DDM Commercial Temperature
SPT-P858G-S3H	Transceiver SFP+ 850nm 8.5G LC 300m with DDM Commercial Temperature HP

Note:

- 1、 If you need -40 ~85°C products, please contact us.
- 2、 If you need more customized services, please contact us.

E-mail: info@sopto.com.cn

Web : <http://www.sopto.com.cn>